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Professor, Tsinghua University

Fellow of IEEE

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May 12, 2023



Education

1983, Bachelor of Science in Electronic Eng, Tsinghua University, P. R. China

1985, Master of Science in Electronic Eng, Tsinghua University, P. R. China

1990, Ph. D. in Electronic Eng. , Tsinghua University

Working Experiences

1997-Now, Full professor , Tsinghua University, China

2014.9-2015.3, Visiting professor, Hong Kong University of Science and Technology

1994-1997, Associate professor, Tsinghua University, China

1993-1994, Visiting researcher, K.U. Leuven, Belgium

1992-1993, Visiting scholar, Carnegie Mellon University, USA

1988-1992, Senior lecturer , Tsinghua University, China

1983-1988, Assistant Professor , Tsinghua University, China

Research Interests & Achievements

RF CMOS circuit design, including RFID, RF Circuit Design, PLL based clock synthesizers, low power wireless transceivers, etc.

Biomedical circuits & systems, including the cutting edge IC technologies & digital signal processing, implantable smart biomedical device for disease diagnosis.

- 15 completed national research projects
- 13 published books and book chapters
- 26 papers in IEEE Journal of Solid-state Circuit (JSSC)
- 48 Papers in IEEE Tran. on Circuits and Systems I and II (TCAS I & II)
- 18 Papers in IEEE Transactions on Biomedical Circuits and Systems(TBioCAS)
- 34 Papers published on other IEEE Transactions
- 124 papers in OTHER Journal and cited by Science Citation Index (SCI)
- 257 papers in other Journals and NOT cited by Science Citation Index
- 12 papers published in International Conference of Solid-state Circuit (ISSCC)
- 112 papers published in IEEE International Symposium on Circuits and Systems (ISCAS)
- 30 papers published in Custom Integrated Circuits Conference (CICC)
- 46 papers published in Asian Solid-State Circuits Conference (A-SSCC)

- 21 papers published in Radio Frequency Integrated Cir. Symp. (RFIC)
- 409 papers published in other International Conferences
- 29 papers published in Chinese Conferences
- Holding 156 Chinese patents and 10 US patents

Teaching Activities

Analysis and Design of Advanced Analog Integrated Circuits	2005, 2006, 2013
The Invention of Transistors and the Birth of Information Age	2005~2021 (every year)
Principles of Digital Integrated System Design	2012, 2017 - 2021
Advances in microelectronics	2004~2021 (every year)
Structural digital Integrated Circuit design	1997-2006 (every year)
Analysis and Design of Advanced Analog integrated Circuits	1998-2003 (every year)
Circuits design for telecommunication	2001, 2002

Honors and Awards

- 2018 First Prize of 2018 Technology Invention Award by the Chinese Institute of Electronics for the contribution of high reliability and low power personal security chip design technology
- 2018 Second Prize of 2018 of Science and Technology Progress Award Promulgated by Zhejiang Province Government for the Application of Low Power Chip Design Technology in Intelligent Measurement System
- 2017 Second Prize of 2017 Technology Invention Award by the Chinese Institute of Electronics for the contribution of multi-level heterogeneous integrated micro medical electronic technology with low-power wireless chip as the core
- 2014 National Teaching Award by the Chinese Ministry of Education for the contribution for the construction of educational system for integrated circuit in China
- 2005 Science and Technology Progress Award Promulgated by the Beijing Municipal Government for the contribution in the design of integrated circuit used in the Chinese national ID card
- 2004 First class Teaching Achievement Award Promulgated by the Beijing Municipal Government for the contribution in the course “MOS IC Design and Practice”
- 2001 Teaching Excellence Award for Young Teachers in Tsinghua University
- 1999 Teaching Achievement Award Promulgated by the Chinese Ministry of Education for the contribution in the course teaching and book of the “Computer Aided Analysis and design of electronic circuits”
- 1997 Academic Achievement Award for Young Researchers of Tsinghua University
- 1997 Outstanding teachers Awarded by the Beijing Municipal Government
- 1992 National Science and Technology Progress Award for the contribution of the design and implementation of the Third Generation ICCAD systems
- 1992 National Science and Technology Progress Award for the contribution of TV-CAD systems and optimization of the circuits in color TV set
- 1991 Science and Technology Progress Award issued by State Board of Education for the contribution of the design of a measurement ASIC by Resistive sensing
- 1991 Teaching Excellence Award for Young Teachers in Tsinghua University
- 1990 Science and Technology Progress Award issued by the Ministry of Machinery and electronics industry for the contribution of TV-CAD systems and optimization of the circuits in color TV set

- 1988 Science and Technology Progress Award issued by the Beijing Municipal Government for the contribution of the development of the circuit simulator TADS-C2
- 1988 Science and Technology Progress Award issued by the Ministry of Electronics industry for the contribution of the development of the circuit simulator TADS-C2

Public Services

- 2000-2016 Deputy Director, Institute of Microelectronics, Tsinghua University (Deputy Chair of The Department of Micro and Nano Electronics)
- 2015 - 2025 Member of the 10th and 11th Academic Committee of Tsinghua University;
- 2011-2014 Members of the 4th Professor Appointment Committee of Tsinghua University
- 2011-2019 Vice Chairman of the "Electronic and Communication Engineering" Subcommittee of the 10th and 11th Degree Evaluation Committee of Tsinghua University
- 2011-2014 Member of the 3rd, 4th, and 5th GEM Listing and Issuance Review Committee of China Securities Regulatory Commission
- 2007.1-2011.12 Chief Scientist of China Ministry of Science, and Technology Expert Committee Member, China 863 Program, in the area of Information Science and Technologies
- 2013-2018 Member of the consulting expert group for National Special Project on kernel electronic devices, high-performance general-purpose IC and basic software products
- 2006~2010 Member of Steering Committee for the Education in Information and Electrical Engineering, Ministry of Education, P. R. China
- 2013~2017 Member of the Steering Committee for the Education of Electronics and Information science, Ministry of Education, P. R.China
- 2018~Now Chairman of the Integrated Circuits for Cryptography Society of Chinese Institute of Cryptographic
- 1999-Now Deputy Secretary General of Integrated Circuit Society in China Semiconductor Industries Association
- 2002-Now Deputy Chair, ASIC Society of Chinese Institute of Communication
- 2001- Now Deputy Chair, Beijing Semiconductor Industries Association
- 2013-Now Member of Computer Aided Surgery Technology Society of China Medical Biotechnology Association
- 2019~2020 Member, IEEE SSCS and CASS Fellow Evaluation Committee
- 2019~2020 Associate Editors in Chief, IEEE Open Journal of Circuits and Systems
- 2016~2021 IEEE CASS Technical Committee member for Biomedical and Life Science Circuits and Systems
- 2018-2019 Distinguished Lecturer of IEEE Solid-State Circuits Society
- 2020-2022 Distinguished Lecturer of IEEE Circuits and Systems Society

- 2016-2018 AdCom Member of the IEEE Solid-State Circuits Society
- 1999-2009 Chair, IEEE Solid-State Circuit Society Beijing Chapter (founding chair)
- 1998-2010 Official Member, Commission C of China National Committee for URSI, (Union Radio-Scientifique Internationale)
- 2016-2019 Associate Editor, IEEE Trans. on Circuits and Systems-I
- 2008-2015 Associate Editor, IEEE Trans. on Biomedical Circuits and Systems
- 2010-2013 Associate Editor, IEEE Trans. on Circuits and Systems-II
- 2004-2015 TPC Member, IEEE International Solid State Circuit Conference (ISSCC)
- 2004-Now Member, Steering Committee of IEEE Asian Solid-State Circuits Conference(A-SSCC)
- 2005-Now TPC Member, International Conference on Solid-State and Integrated-Circuit Technology (ICSICT)
- 2013 TPC Chair, IEEE Asian Solid-State Circuits Conference(ASSCC)
- 2007~09, 2011 TPC Member, IEEE GREAT LAKES SYMPOSIUM ON VLSI (GLSVLSI)
- 2008 TPC Member, IEEE Asia Pacific Conference on Circuits and Systems (APCCAS)
- 2007 TPC Member, IEEE Int'l Workshop on Radio-Frequency Integration Technology
- Dec 2006, Dec 2009, Nov 2014 Guest Editor, Journal of Solid-State Circuits,
- 2009, 2015 General Co-Chair, IEEE Biomedical Circuits and Systems Conference (BioCAS)
- 2011 General Chair, IEEE Int'l Workshop on Radio-Frequency Integration Technology
- 2006 and 2010 Organization Committee Chair, IEEE Asian Solid-State Circuits Conference,

Book and book chapters (5 in Chinese and 8 in English)

(a) Books published in English – (8)

1. Shaojie Su, Hong Chen, Hanjun Jiang, **Zhihua Wang**, A Low-Power Vision- and IMU-Based System for the Intraoperative Prosthesis Pose Estimation of Total Hip Replacement Surgeries, Chapter 13 of "Low Power Semiconductor Devices and Processes for Emerging Applications in Communications, Computing, and Sensing", (Edited by Sumeet Walia), CRC Press, Taylor & Francis Group, 2018
2. Weitao Li, Fule Li, **Zhihua Wang**, High-Resolution and High-Speed Integrated CMOS AD Converters for Low-Power Applications, (ISBN: 978-3-319-62011-4), Springer Press, 2017
3. **Zhihua Wang**, Hanjun Jiang, Hong Chen, CMOS IC Design for Wireless Medical and Health Care, (ISBN: 978-1-4614-9502-4), Springer Press, 2013
4. Nianxiong Nick Tan, Dongmei Li, **Zhihua Wang** (Editors), Ultra-Low Power Integrated Circuit Design - Circuits, Systems, and Applications, (ISBN 978-1-4419-9972-6), Springer Press, 2014
5. Tianjia Sun, Xiang Xie, **Zhihua Wang**, Wireless Power Transfer for Medical Microsystems (ISBN: 978-1-4614-7701-3), Springer Press, 2013
6. **Zhihua Wang**, Xiang Xie, Xinkai Chen, Xiaowen Li, Design Consideration of Low Power

Digital Integrated Systems, Chapter 4 of “CMOS Biomicrosystems, Where electronics meet biology”, (Edited by Krzysztof Iniewski), A Jone Willey & Sons, Inc, Publication, 2011

7. Woogeun Rhee, **Zhihua Wang**, Sigma-Delta Fractional-N Phase-looked Loop, Chapter 12 of “CMOS Nanoelectronics, Analog and RF VLSI circuits”, (Edited by Krzysztof Iniewski), The McGraw-Hill Companies, Inc., 2011
8. Ni Xu, Woogeun Rhee, **Zhihua WANG**, Hybrid Phase Modulators with Enhanced Linearity, Chapter 17 of “Wireless Transceiver Circuits – System Perspective and Design Aspects” (Edited by Woogeun Rhee), CRC Press, Taylor & Francis Group, 2015

(b) Books published in Chinese – (5)

9. Xiang Xie, Ziqiang WANG, Hanjun Jiang, **Zhihua WANG**, Theory and Clinical Application of Capsule Endoscope System, Science Press, 2010 (in Chinese)
10. Zaiwang Dong, Dongmei Li, **Zhihua WANG**, Yongming LI, Advanced Analog Integrated Circuits, Tsinghua University Press, 2006 (in Chinese)
11. **Zhihua WANG**, Yangdong Deng, Structural Design and High-Level Synthesis of Digital Integrated System, Tsinghua University Press, 2000 (in Chinese)
12. Zucheng Zou, **Zhihua WANG**, Methodology of Design Automation for Application Specific Integrated Circuits and Systems, Defense Industry Press Publication, 1997, (in Chinese)
13. Hui WANG, **Zhihua WANG**, Computer Aided Analysis and Design of Electronic Circuits, Tsinghua University Press, 1996 (in Chinese)

Journal Papers Cited by Science Citation Index – (250 in Total)

IEEE Journal of Solid-State Circuits (JSSC) – (26)

1. Yaqian Sun, Wei Deng, Haikun Jia, Yejun He, **Zhihua Wang**, Baoyong Chi, A Compact and Low Phase Noise Square-Geometry Quad-Core Class-F VCO Using Parallel Inductor-Sharing Technique, IEEE Journal of Solid-State Circuits, Early Access Articles, Vol. PP(99), pp. 1-13, APR 2023
2. Wei Deng, Zipeng Chen, Haikun Jia, Pingda Guan, Taikun Ma, Angxiao Yan, Shiyan Sun, Xiangrong Huang, Guopei Chen, Ruichang Ma, Shengnan Dong, Luqiang Duan, **Zhihua Wang**, Baoyong Chi, A D-Band Joint Radar-Communication CMOS Transceiver, IEEE Journal of Solid-State Circuits, Vol.58(2), pp. 411-427, JAN 2023
3. Haikun Jia, Pingda Guan, Wei Deng, **Zhihua Wang**, Baoyong Chi, A Low-Phase-Noise Quad-Core Millimeter-Wave Fundamental VCO Using Circular Triple-Coupled Transformer in 65-nm CMOS, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.58(2), pp. 371-385, JAN 2023
4. Yanshu Guo, Zhongyuan Fang, Kai Tang, Zhaoyang Weng, Chuanshi Yang, Nan Wang, Eldwin J. Ng, **Zhihua Wang**, Chun-Huat Heng, Hanjun Jiang, Yuanjin Zheng, A 164- μ W 915-MHz Sub-Sampling Phase-Tracking Zero-IF Receiver With 5-Mb/s Data Rate for Short-Range Applications, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.57, No.9, pp. 2658 - 2671, SEP 2022
5. Wei Deng, Zipeng Chen, Haikun Jia, Angxiao Yan, Shiyan Sun, Guopei Chen, **Zhihua Wang**, Baoyong Chi, A Self-Adapted Two-Point Modulation Type-II Digital PLL for Fast Chirp Rate and Wide Chirp-Bandwidth FMCW Signal Generation, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.57(4), pp. 1162 - 1174, APR 2022
6. Haixin Song, Dang Liu, Yining Zhang, Woogeun Rhee, **Zhihua Wang**, A 6.5-8.1-GHz Communication/Ranging VWB Transceiver for Secure Wireless Connectivity With Enhanced Bandwidth Efficiency and $\Delta\Sigma$ Energy Detection, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.55(2), pp. 219-232, DEC 2020
7. Deng Luo, Milin Zhang, **Zhihua Wang**, A Low-Noise Chopper Amplifier Designed for Multi-

- Channel Neural Signal Acquisition, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.54(8), pp. 2255-2265, AUG 2019
8. Yining Zhang, Meng Ni, Xiaohua Huang, Woogeun Rhee, **Zhihua Wang**, A 3.7-mW 2.4-GHz Phase-Tracking GFSK Receiver With BBPLL-Based Demodulation, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.54(2), pp. 336-345, FEB 2019
 9. Jianfu Lin, Zheng Song, Nan Qi, Woogeun Rhee, **Zhihua Wang**, Baoyong Chi, A 77-GHz Mixed-Mode FMCW Signal Generator Based on Bang-Bang Phase Detector, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.53(10), pp. 2850-2863, OCT 2018
 10. Yining Zhang, Ranran Zhou, Woogeun Rhee, **Zhihua Wang**, A 1.9-mW 750-kb/s 2.4-GHz F-OFDM Transmitter With Symmetric FM Template and High-Point Modulation PLL, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.52(10), pp. 2627-2635, OCT 2017
 11. Dang Liu, Xuwen Ni, Ranran Zhou, Woogeun Rhee, **Zhihua Wang**, A 0.42-mW 1-Mb/s 3-to-4-GHz Transceiver in 0.18- μ m CMOS With Flexible Efficiency, Bandwidth, and Distance Control for IoT Applications, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.52(6), pp. 1479-1494, JUN 2017
 12. Xuqiang Zheng, Chun Zhang, Fangxu Lv, Feng Zhao, Shuai Yuan, Shigang Yue, Ziqiang Wang, Fule Li, **Zhihua Wang**, Hanjun Jiang, A 40-Gb/s Quarter-Rate SerDes Transmitter and Receiver Chipset in 65-nm CMOS, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.52(11), pp. 2963-2978, NOV 2017
 13. Haikun Jia, Lixue Kuang, Wei Zhu, Zhiping Wang, Feng Ma, **Zhihua Wang**, Baoyong Chi, A 77 GHz Frequency Doubling Two-Path Phased-Array FMCW Transceiver for Automotive Radar, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.51(10), pp. 2299-2311, OCT 2016
 14. Xiaobao Yu, Meng Wei, Yun Yin, Ying Song, Siyang Han, Qiongbing Liu, Zongming Jin, Xiliang Liu, **Zhihua Wang**, Yichuang Sun, Baoyong Chi, A Fully-Integrated Reconfigurable Dual-Band Transceiver for Short Range Wireless Communications in 180 nm CMOS, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 50 (11), pp. 2572-2590, NOV 2015
 15. Shuli Geng, Dang Liu, Yanfeng Li, Huiying Zhuo, Woogeun Rhee, **Zhihua Wang**, A 13.3 mW 500 Mb/s IR-UWB Transceiver With Link Margin Enhancement Technique for Meter-Range Communications, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 50(3), pp. 669-678, MAR 2015
 16. **Zhihua Wang**, Introduction to the Special Section on the 2013 IEEE Asian Solid-State Circuits Conference (A-SSCC), IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol.49(11), pp. 2375-2376, NOV 2014
 17. Ni Xu, Woogeun Rhee, **Zhihua Wang**, A Hybrid Loop Two-Point Modulator Without DCO Nonlinearity Calibration by Utilizing 1 Bit High-Pass Modulation, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 49(10), pp. 2172-2186, OCT 2014
 18. Cong Shi, Jie Yang, Ye Han, Zhongxiang Cao, Qi Qin, Liyuan Liu, Nan-Jian Wu, **Zhihua Wang**, A 1000 fps Vision Chip Based on a Dynamically Reconfigurable Hybrid Architecture Comprising a PE Array Processor and Self-Organizing Map Neural Network, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol 49(9), pp. 2067-2082, SEP 2014
 19. Lingwei Zhang, Hanjun Jiang, Jianjun Wei, Jingjing Dong, Fule Li, Weitao Li, Jia Gao, Jianwei Cui, Baoyong Chi, Chun Zhang, **Zhihua Wang**, A Reconfigurable Sliding-IF Transceiver for 400 MHz/2.4 GHz IEEE 802.15.6/ZigBee WBAN Hubs With Only 21% Tuning Range VCO, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 48(11), pp. 2705-2716, NOV 2013
 20. Vadim Gutnik, Nikolaus Klemmer, **Zhihua Wang**, Michael Green, Roland Thewes, Introduction to the Special Issue on the 2009 IEEE International Solid-State Circuits Conference, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol 44(12), pp. 3227-3231, DEC 2009
 21. Li Zhang, Xueyi Yu, Yuanfeng Sun, Woogeun Rhee, Dawn Wang, **Zhihua Wang**, Hongyi

- Chen, A Hybrid Spur Compensation Technique for Finite-Modulo Fractional- Phase-Locked Loops, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL44(11), pp 2922-2934, NOV 2009
22. Xueyi Yu, Yuanfeng Sun, Woogeun Rhee, **Zhihua Wang**, An FIR-Embedded Noise Filtering Method for $\Delta\Sigma$ Fractional-N PLL Clock Generators, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 44, NO. 9, pp 2426-2436, SEPTEMBER 2009
 23. Xueyi Yu, Yuanfeng Sun, Woogeun Rhee, Hyung Ki Ahn, Baoyongeong-Ha Park, **Zhihua Wang**, A $\Delta\Sigma$ Fractional-N Synthesizer With Customized Noise Shaping for WCDMA/HSDPA Applications, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 44, NO. 8, pp 2193-2201, AUGUST 2009
 24. Xiang Xie, Guolin Li, Xinkai Chen, Xiaowen Li, **Zhihua Wang**, A low-power digital IC design inside the wireless endoscopic capsule, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 41 (11): 2390-2400 NOV 2006
 25. Michael P. Flynn, Philip K. T. Mok, **Zhihua Wang**, Wolfgang A. Pribyl, Johannes Solhusvik, Introduction to the special issue on the 2006 IEEE International Solid-State Circuits Conference, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 41 (12), pp. 2627-2631, DEC 2006
 26. Leibo Liu, N Chen, HY Meng, L Zhang, **Zhihua Wang**, HY Chen, A VLSI architecture of JPEG2000 encoder, IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 39 (11): 2032-2040 NOV 2004

IEEE Transactions on Circuits and Systems I and II (TCAS I & II) – (48)

27. Heng Huang, Xiliang Liu, Zijian Tang, Wei Song, Yuwei Zhang, Xiaoyan Ma, Milin Zhang, Jintao Wang, **Zhihua Wang**, Guolin Li, A 2 nJ/bit, 2.3% FSK Error Fully Integrated Sub-2.4 GHz Transmitter With Duty-Cycle Controlled PA for Medical Band, IEEE Transactions on Circuits and Systems I: Regular Papers, Vol.69(12), pp. 5018-5029, DEC 2022
28. Qian Chen, Zelu Luo, Huan Deng, Xinfu Zheng, Xiaodong You, Pengyi Cao, Xinpeng Xing, Haigang Feng, **Zhihua Wang**, A Single Chain 800M/1.8G/2.4GHz Multistandard Transceiver With Multibranch Transformer for Low-Cost IoT Applications, IEEE Transactions on Circuits and Systems II: Express Briefs, Vol.69(12), pp. 4684-4688, DEC 2022
29. Chao Zhang, Yiqiao Liao, Siqi Han, Milin Zhang, **Zhihua Wang**, Xiang Xie, Multichannel Multidomain-Based Knowledge Distillation Algorithm for Sleep Staging With Single-Channel EEG, IEEE Transactions on Circuits and Systems II: Express Briefs, Vol.69(11), pp. 4608-4612, OCT 2022
30. Yaqian Sun, Wei Deng, Haikun Jia, **Zhihua Wang**, Baoyong Chi, A 4.4-GHz 193.2-dB FoM 8-Shaped-Inductor Based LC-VCO Using Orthogonal-Coupled Triple-Coil Transformer, IEEE Transactions on Circuits and Systems II: Express Briefs, Vol.69(10), pp. 4028-4032, SEP 2022
31. Liqun Feng, Woogeun Rhee, **Zhihua Wang**, A Quantization Noise Reduction Method for Delta-Sigma Fractional-N PLLs Using Cascaded Injection-Locked Oscillators, IEEE Transactions on Circuits and Systems II: Express Briefs, Vol.69(5), pp. 2448-2452, MAY 2022
32. Dengjie Wang, Ziqiang Wang, Hao Xu, Jiawei Wang, Zeliang Zhao, Chun Zhang, **Zhihua Wang**, Hong Chen, A 56-Gbps PAM-4 Wireline Receiver With 4-Tap Direct DFE Employing Dynamic CML Comparators in 65 nm CMOS, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I: REGULAR PAPERS, Vol.69(3), pp. 1027-1040, MAR 2022
33. Meng Ni, Xiao Wang, Fule Li, Woogeun Rhee, **Zhihua Wang**, A 13-Bit 2-GS/s Time-Interleaved ADC With Improved Correlation-Based Timing Skew Calibration Strategy, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I: REGULAR PAPERS, Vol.69(2), pp. 481 - 494, FEB 2022
34. Xinyu Xu, Zixiang Wan, Woogeun Rhee, **Zhihua Wang**, A Bias-Current-Free Fractional-N Hybrid PLL for Low-Voltage Clock Generation, IEEE TRANSACTIONS ON CIRCUITS

- AND SYSTEMS I: REGULAR PAPERS, Vol.68(9), pp. 3611-3620, JUN 2021
35. Kang Zhao, Shulin Feng, Hanjun Jiang, **Zhihua Wang**, Ping Chen, Binjie Zhu, Xianglong Duan, A Binarized CNN-based Bowel Sound Recognition Algorithm with Time-domain Histogram Features for Wearable Healthcare Systems, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.69(2), pp. 629-633, FEB 2022
 36. Yiqiao Liao, Chao Zhang, Milin Zhang, **Zhihua Wang**, Xiang Xie, LightSleepNet: Design of a Personalized Portable Sleep Staging System Based on Single-Channel EEG, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.69(1), pp.224-228, JAN 2022
 37. Jiahao Zhao, Yining Zhang, Kunhong Zeng, Woogeun Rhee, **Zhihua Wang**, A 2.4-GHz Crystal-Less GFSK Receiver Using an Auxiliary Multiphase BBPLL for Digital Output Demodulation With Enhanced Frequency Scaling, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, **Early Access Articles**, Vol.69(9), pp. 1-1, OCT 2020
 38. Zipeng Chen, Wei Deng, Haikun Jia, Yibo Liu, Jianxi Wu, Pingda Guan, Jinyu Zhu, Luhong Mao, **Zhihua Wang**, Baoyong Chi, A U-Band PLL Using Implicit Distributed Resonators for Sub-THz Wireless Transceivers in 40 nm CMOS, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.67(9), pp. 1574-1578, JUN 2020
 39. Xiao Wang, Fule Li, **Zhihua Wang**, A Simple Histogram-based Capacitor Mismatch Calibration in SAR ADCs, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, **Early Access Articles**, Vol.69(9), pp. 1-1, APR 2020
 40. Kan Peng, Xian Tang, Songping Mai, **Zhihua Wang**, A Simultaneous Power and Downlink Data Transfer System With Pulse Phase Modulation, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.66(5), pp. 808-812, MAY 2019
 41. Xiangyu Meng, Baoyong Chi, Yibo Liu, Taikun Ma, **Zhihua Wang**, A Fully Integrated 150-GHz Transceiver Front-End in 65-nm CMOS, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.66(4), pp. 602-606, APR 2019
 42. Xiao Wang, Fule Li, Wen Jia, **Zhihua Wang**, A 14-Bit 500-MS/s Time-Interleaved ADC With Autocorrelation-Based Time Skew Calibration, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.66(3), pp. 322-326, MAR 2019
 43. Yining Zhang, Haixin Song, Ranran Zhou, Woogeun Rhee, Inbo Shim, **Zhihua Wang**, A Capacitor-Less Ripple-Less Hybrid LDO With Exponential Ratio Array and 4000x Load Current Range, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.66(1), pp. 36-40, JAN 2019
 44. Yanshu Guo, Hanjun Jiang, Heng Liu, Zhaoyang Weng, Woogeun Rhee, Chun Zhang, **Zhihua Wang**, A 120 pJ/bit $\Delta\Sigma$ -Based 2.4-GHz Transmitter Using FIR-Embedded Digital Power Amplifier, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.65(12), pp. 1854-1858, DEC 2018
 45. Haikun Jia, Clarissa C. Prawoto, Baoyong Chi, **Zhihua Wang**, C. Patrick Yue, A Full Ka-Band Power Amplifier With 32.9% PAE and 15.3-dBm Power in 65-nm CMOS, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I: REGULAR PAPERS, Vol.65(9), pp. 2657-2668, SEP 2018
 46. Xiaohua Huang, Dang Liu, Woogeun Rhee, **Zhihua Wang**, A 1-GHz 1.6-mW Auto-Calibrated Bit Slicer for Energy/Envelope Detection Receivers, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.65(5), pp. 587-591, MAY 2018
 47. Zhaoyang Weng, Hanjun Jiang, Jingjing Dong, Yang Li, Jingyi Zheng, Yiyu Shen, Fule Li, Woogeun Rhee, **Zhihua Wang**, 400-MHz/2.4-GHz Combo WPAN Transceiver IC for Simultaneous Dual-Band Communication With One Single Antenna, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I: REGULAR PAPERS, Vol.65(2), pp. 745-757, FEB 2018
 48. Fei Chen, Woogeun Rhee, **Zhihua Wang**, A 5-mW 750-kb/s Noninvasive Transceiver for Around-the-Head Audio Applications, IEEE TRANSACTIONS ON CIRCUITS AND

- SYSTEMS II: EXPRESS BRIEFS, Vol.65(2), pp. 196-200, FEB 2018
49. Xican Chen, Yiyu Shen, Zhicheng Wang, Woogeun Rhee, **Zhihua Wang**, 17 mW 3-to-5 GHz Duty-Cycled Vital Sign Detection Radar Transceiver With Frequency Hopping and Time-Domain Oversampling, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I-REGULAR PAPERS, Vol.64(4), pp. 969-980, APR 2017
 50. Xuqiang Zheng, Zhijun Wang, Fule Li, Feng Zhao, Shigang Yue, Chun Zhang, **Zhihua Wang**, A 14-bit 250 MS/s IF Sampling Pipelined ADC in 180 nm CMOS Process, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I: REGULAR PAPERS, Vol.63(9), pp. 1381-1392, SEP 2016
 51. Shuai Yuan, Liji Wu, Ziqiang Wang, Xuqiang Zheng, Chun Zhang, **Zhihua Wang**, A 70 mW 25 Gb/s Quarter-Rate SerDes Transmitter and Receiver Chipset With 40 dB of Equalization in 65 nm CMOS Technology, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I: REGULAR PAPERS, Vol.63(7), pp. 939-949, JUL 2016
 52. Xiaobao Yu, Meng Wei, Ying Song, **Zhihua Wang**, Baoyong Chi, A PAPR-Aware Dual-Mode Subgigahertz CMOS Power Amplifier for Short-Range Wireless Communication, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol.63(1), pp. 44-48, JAN 2016
 53. Yudong Zhang, Woogeun Rhee, Taeik Kim, Hojin Park, **Zhihua Wang**, A 0.35–0.5-V 18–152 MHz Digitally Controlled Relaxation Oscillator With Adaptive Threshold Calibration in 65-nm CMOS, IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II: EXPRESS BRIEFS, Vol. 62 (8), pp. 736 - 740, AUG 2015
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